Implementation of Vedic Mathematics Algorithms in the Field of VLSI : A Survey

Parveen Kaur[1], Dr. Bhupinder Singh Dhaliwal[2]

[1]*Ph.D Research Scholar,Department of ECE GKU, Talwandi Sabo,Bathinda, Punjab(India)*

[2]*Professor, Department of ECE GKU, Talwandi Sabo,Bathinda, Punjab(India)*

ABSTRACT

Vedic mathematics is the most proficient alternative to conventional matahematics as it simplifies and converts larger computation to smaller one. Vedic Algorithm can also be applied on Binary Arithmetic operations also. Basic mathematical operations play a pivotal role in the field of digital signal processing, image processing, computer graphics, application-specific (embedded) system, cryptography etc. This paper presents a survey on the implementation of different Vedic mathematical algorithms for digital signal processing, image processing and other heavy computational applications.